

CLIENT-SERVER SEMICONDUCTOR VERIFICATION SYSTEM

ABSTRACT

A client-server semiconductor verification system is described. The system comprises a client device storing a test job having test vectors and configuration data for programmable logic for testing a design of a logic circuit. A server is coupled to the client device and receives the test job from the client device. Finally, a system under test coupled to the server and has programmable logic which is reconfigured. The system under test receives the test vectors and outputs result vectors to the client device by way of the server. Corresponding methods are also disclosed.